Amendments to the Specification:

Please replace the paragraph beginning on page 2, line 21, and ending on page 3, line 16, with the following amended paragraph:

Fig. 15 is a timing diagram of the aforementioned block diagram, showing that when the VIGN voltage 3a is changed from an LO signal to an HI signal, the signal 7a controlling the power source IC 6 is also changed immediately to an HI signal. At this time, the power source IC 6 is switched to the operation state and starts supply of the inner constant voltage (VCC) 6a into the electronic control unit for car 1. Further, when the VIGN voltage 3a is changed from the HI signal to the **[[HI]]** LO signal, the CPU 9 detects it and the control signal 9a from the CPU 9 puts the power source IC 6 into the non-operation state after a lapse of a predetermined time of Delay 1 to interrupt the supply of the inner constant voltage (VCC) 6a. However, when the line of the control signal 9a from the CPU 9 is fixed to high due to an unexpected situation, even if the VIGN voltage 3a is changed from the HI signal to the LO signal, the signal 7a controlling the power source IC 6 is fixed to high, thereby cannot put the power source IC 6 into the standby state. Therefore, by a battery voltage 4a, the power source IC 6 cannot be put into the non-operation state. As a result, the

inner constant voltage (VCC) 6a is always supplied into the electronic control unit for car 1. It is indicated by a dotted line (right half) shown in Fig. 15.